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Serial Number *10619918*	Application No. 10/619,916	Applicant(s) FARNWORTH, WARREN M.	

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The term of this patent shall not extend beyond the expiration date of U.S. Patent No:	6,432,752	6,593,171			
The term of this patent subsequent to the adjacent date has been disclaimed.					
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Docket Number (Optional)
2209-4212,608

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JUL 25 2004

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In re Application of: Warren M. Farnworth
Application No.: 10/019,918
Filed: July 15, 2003

For: STEREO LITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE
AND SEMICONDUCTOR DEVICES INCLUDING STEREO LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

The owner, Micron Technology, Inc., of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 6,593,171. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 to 156 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims cancelled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

2. ☒ The undersigned is an attorney of record.

Brick G. Power

July 14, 2004

Signature

Date

Brick G. Power

Typed or printed name
Reg. No. 38,581

- ☒ The Commissioner is authorized to charge Terminal Disclaimer fee under 37 CFR 1.20(d) to Deposit Account 20-1469.

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Docket Number (Optional)
2269-4212-605

**TERMINAL DISCLAIMER
APPROVED**

In re Application of: Warren M. Farnworth

Application No.: 10/619,918

Filed: July 15, 2003

JUL 25 2004

For: STEREO LITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES
AND SEMICONDUCTOR DEVICES INCLUDING STEREO LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES
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